

AMENDMENTS TO THE CLAIMS

Please amend claims 9 and 10 as indicated:

1. (previously amended) A system for downloading a data file from a web server to a user workstation through a network to which is connected said user workstation, having a hard disk for storing said data file being transferred over a SCSI bus; said user workstation including a dual-port memory, said dual-port memory for storing temporarily said data file having an input port and an output port; a network logic unit interconnected between said network and said input port for receiving said data file from said network and transmitting it to said dual-port memory; and a SCSI logic unit interconnected between said output-port and said SCSI bus for transferring said data file from said dual-port memory to said hard disk over said SCSI bus.

Claims 2-8. (cancelled)

9. (currently amended) A system comprising:

a volatile system memory;

a non-volatile memory; [[and]]

a network adapter, the network adapter including:

a non-system memory capable of temporarily storing a packet received by the network adapter; and

a microcontroller capable of evaluating the packet received by the network adapter; and

a system bus connecting the network adapter to the volatile system memory,

wherein if the microcontroller determines that the packet is destined for the system's non-volatile memory, then the microcontroller bypasses the system bus for the volatile system memory and directly communicates with the non-volatile memory to transfer [[transfers]] the packet from the non-system memory in the network adapter to the system's non-volatile memory, and wherein if the microcontroller does not determine that the packet is destined for the system's non-volatile memory, then the microcontroller transfers the packet from the non-system memory in the network adapter to the volatile system memory.

10. (currently amended) The system of claim 9, further comprising:

[[a system bus connecting the network adapter to the volatile system memory; and]]

a Small Computer System Interface (SCSI) bus connecting the network adapter to the system's non-volatile memory,

wherein a transfer of the packet from the non-system memory in the network adapter to the system's non-volatile memory does not occur via the system bus.

11. (previously presented) The system of claim 10, wherein the non-system memory in the network adapter is a dual-port memory, the dual-port memory having a first port coupled to the system bus and a second port coupled to the SCSI bus.

12. (previously presented) The system of claim 9, wherein the system's non-volatile memory is a hard disk in a hard disk drive that has a SCSI interface to the SCSI bus.

13. (previously presented) The system of claim 9, wherein the microcontroller evaluates the packet by examining in the packet:

an address source;

an address destination; and

a port number that indicates which transfer protocol is used by the packet,

such that only packets having a pre-determined source and address destination and using a pre-determined port are transferred from the non-system memory in the network adapter to the system's non-volatile memory.

14. (previously presented) The system of claim 9, wherein the microcontroller locally stores a listing of address sources, address destinations and port numbers that authorize the packet to be routed directly to the system's non-volatile memory.

15. (previously presented) The system of claim 9, wherein the packet is received from a network.

16. (previously presented) A network adapter comprising:

a non-system-memory capable of temporarily storing a packet received by the network adapter; and

a microcontroller capable of evaluating the packet received by the network adapter; wherein if the microcontroller determines that the packet is destined for a non-volatile memory in a system, then the microcontroller transfers the packet from the non-system memory in the network adapter to the non-volatile memory, and wherein if the microcontroller does not determine that the packet is destined for the non-volatile memory, then the microcontroller transfers the packet from the non-system memory in the network adapter to a volatile system memory.